

## Synchronous Serial Communication Boards

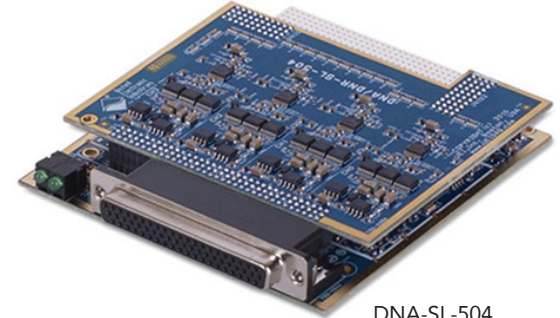
The following FAQ provides an overview of UEI synchronous serial communication boards: the DNx-SL-504, DNx-CT-602-804, and DNx-SL-514.

### SYNCHRONOUS SERIAL BOARD OVERVIEW

The SL-504, CT-602-804, and SL-514 all send and receive data as bit streams, with bit transmission and reception synchronized to a common serial clock signal.

All three boards support RS-422/RS-485 electrical standards, which specify the physical electrical specifications for signal lines, such as differential signal: using twisted pair cables, voltage levels, and bit rates. The SL-504 also supports the RS-232 electrical standard.

The boards differ in the serial protocols they support. This affects whether or not the clock must be a continuous clock stream, what bit pattern signifies the start of a transmission, or what data word lengths are supported.

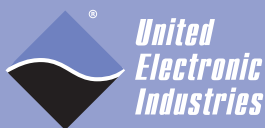


DNx-SL-504

### SL-504, CT-602-804, AND SL-514 FEATURE COMPARISON

Board Type	Electrical Standards	Serial Protocols	Subset of Features
<a href="#">DNx-SL-504</a>	RS-232 RS-485/422	HDLC or SDLC (in RS-485/422 mode)	<ul style="list-style-type: none"> <li>-Programmable bit rate:</li> <li>-RS-232 max baud rate is 230 kbaud</li> <li>-RS-485/422 max baud rate is 4 Mbaud</li> <li>-RS-485/422 full duplex support</li> <li>-RS-485/422 half duplex support (-801)</li> <li>-5,6,7 or 8 bits/word(character)</li> </ul>
<a href="#">DNx-CT-602-804</a>	RS-485/422	General purpose Synchronous Serial (GPSS) or "Clock/Data/Strobe"	<ul style="list-style-type: none"> <li>-Programmable bit rate: 300 baud to 16 Mbaud (2Mbaud max sustained)</li> <li>-Differential, simplex (each port is only RX or TX)</li> <li>-Programmable word length: 3 to 32 bits</li> <li>-Programmable frame sync strobe (frame sync identifies valid data shifting)</li> <li>-Requires continuous clock signal</li> </ul>
<a href="#">DNx-SL-514</a>	RS-422	Synchronous Serial Interface (SSI)	<ul style="list-style-type: none"> <li>- Programmable bit rate: up to 2.5 Mbaud</li> <li>-Differential, simplex (each channel provides independent master port and slave port)</li> <li>-Programmable word length: 3 to 32 bits</li> <li>-Clock train initiates data shifting (clock will not be continuous)</li> </ul>

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