



UEI-STP-AO-200

—

User Manual

**8-Channel, unity gain buffer, screw-terminal panel (STP-AO-200)
and unity gain buffer board with optional DC/DC converter (STP-AO-200D)
for the PowerDNA Cube and PowerDNR RACKtangle**

Release 4.5

February 2012

PN Man-UEI-STP-AO-200-212

No part of this publication may be reproduced, stored in a retrieval system, or transmitted, in any form by any means, electronic, mechanical, by photocopying, recording, or otherwise without prior written permission.

Information furnished in this manual is believed to be accurate and reliable. However, no responsibility is assumed for its use, or for any infringement of patents or other rights of third parties that may result from its use.

All product names listed are trademarks or trade names of their respective companies.

See the UEI website for complete terms and conditions of sale:

<http://www.ueidaq.com/cms/terms-and-conditions/>

Contacting United Electronic Industries

Mailing Address:

27 Renmar Avenue
Walpole, MA 02081
U.S.A.

For a list of our distributors and partners in the US and around the world, please see

<http://www.ueidaq.com/partners/>

Support:

Telephone: (508) 921-4600

Fax: (508) 668-2350

Also see the FAQs and online "Live Help" feature on our web site.

Internet Support:

Support: support@ueidaq.com

Web-Site: www.ueidaq.com

FTP Site: <ftp://ftp.ueidaq.com>

Product Disclaimer:

WARNING!

DO NOT USE PRODUCTS SOLD BY UNITED ELECTRONIC INDUSTRIES, INC. AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS.

Products sold by United Electronic Industries, Inc. are not authorized for use as critical components in life support devices or systems. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness. Any attempt to purchase any United Electronic Industries, Inc. product for that purpose is null and void and United Electronic Industries Inc. accepts no liability whatsoever in contract, tort, or otherwise whether or not resulting from our or our employees' negligence or failure to detect an improper purchase.

Specifications in this document are subject to change without notice. Check with UEI for current status.

Table of Contents

Chapter 1 Introduction	3
1.1 Organization of Manual	3
1.2 The STP-AO-200 and -200D Terminal Panels	5
1.3 Features	6
1.4 Layout	6
1.5 Specification	7
1.6 Wiring & Connectors	8



List of Figures

Chapter 1 – Introduction	1
1-1 The UEI-STP-AO-200 Current Buffer Terminal Panel	6
1-2 Pinout of UEI-STP-AO-200 input connector	8
A-1 Photo of DNA-CBL-37 Flat Ribbon Cable	9
A-2 Photo of DNA-CBL-37 Round, Shielded Cable	9



Chapter 1 Introduction

This document outlines the feature-set of the UEI-STP-AO-200 buffer board.

1.1 Organization of Manual

This UEI-STP-AO-200 User Manual is organized as follows:

- **Introduction**
This section provides an overview of the UEI-STP-AO-200 buffer board features, device architecture, and connectivity.
- **Appendix A - Accessories**
This appendix provides a list of accessories available for use with the UEI-STP-AO-200 current buffer board.
- **Index**
This is an alphabetical listing of the topics covered in this manual.

Manual Conventions

To help you get the most out of this manual and our products, please note that we use the following conventions:



Tips are designed to highlight quick ways to get the job done or to reveal good ideas you might not discover on your own.

NOTE: Notes alert you to important information.



CAUTION! Caution advises you of precautions to take to avoid injury, data loss, and damage to your boards or a system crash.

Text formatted in **bold** typeface generally represents text that should be entered verbatim. For instance, it can represent a command, as in the following example: “You can instruct users how to run setup using a command such as **setup.exe**.”

Text formatted in *fixed* typeface generally represents source code or other text that should be entered verbatim into the source code, initialization, or other file.

Examples of Manual Conventions



Before plugging any I/O connector into the Cube or RACKtangle, be sure to remove power from all field wiring. Failure to do so may cause severe damage to the equipment.

Usage of Terms



Throughout this manual, the term “Cube” refers to either a PowerDNA Cube product or to a PowerDNR RACKtangle™ rack mounted system, whichever is applicable. The term DNR is a specific reference to the RACKtangle, DNA to the PowerDNA I/O Cube, and DNx to refer to both.

1.2 The STP-AO-200 and -200D Terminal Panels

The UEI-STP-AO-200 is a unity gain, high current output buffer board for use with DNA-AO-308 and PD2/PDXI series analog output boards. The unit provides a gain of $1 \pm 0.2\%$ and can drive continuous output currents up to 250 mA on each of the 8 channels up to ± 10 Volt. 500 mA resettable fuses are provided on each output and the output buffers themselves are protected from thermal overload.

The board provides excellent gain and offset performance and is an ideal solution in applications requiring both high current and high accuracy. The 1 V/ μ S slew rate provide ensures the UEI-STP-AO-200 is fast enough to keep up with almost all high current system requirements. The outputs are designed to drive capacitive and inductive loads and should be compatible with almost all systems, including those driving long distances of wire

The 37-pin D input to the UEI-STP-AO-200 can be connected to the DNA-AO-308 board using a standard, straight connection 37-pin cable. PD2/PDXI series boards are connected to the unit via UEI's PD-5B-CONN interconnect boards.

A fan mounted on the unit assures the buffer stays within temperature limits. The fan is thermostatically controlled and turns on if the internal temperature of the unit exceeds 45°C.

Separate LEDs illuminate to display over current conditions at approximately 280 mA. A third LED per channel illuminates to indicate the output buffer is in thermal overload.

The UEI-STP-AO-200 requires a ± 13 to ± 15 volt bipolar power supply with adequate capability to support the output current. The UEI-STP-AO-200D includes a DC/DC converter and allows the buffer to be powered by a single, unipolar supply from +9 to +36V.

The UEI-STP-AO-200 is fully enclosed. Though there is no cover on the top of the unit, the top of the printed circuit board has no active signals exposed other than those on the various connectors and screw terminals..

1.3 Features

The common features of the UEI-STP-AO-200 are listed below:

- Eight (8) independent channels
- 250 mA per channel, continuous
- Resettable output fuse on each channel
- ± 10 Volt output, minimum
- High Accuracy
- Wide power supply input range
- Drives capacitive and inductive loads

1.4 Layout

A photo of the UEI-STP-AO-200 unit is illustrated below.

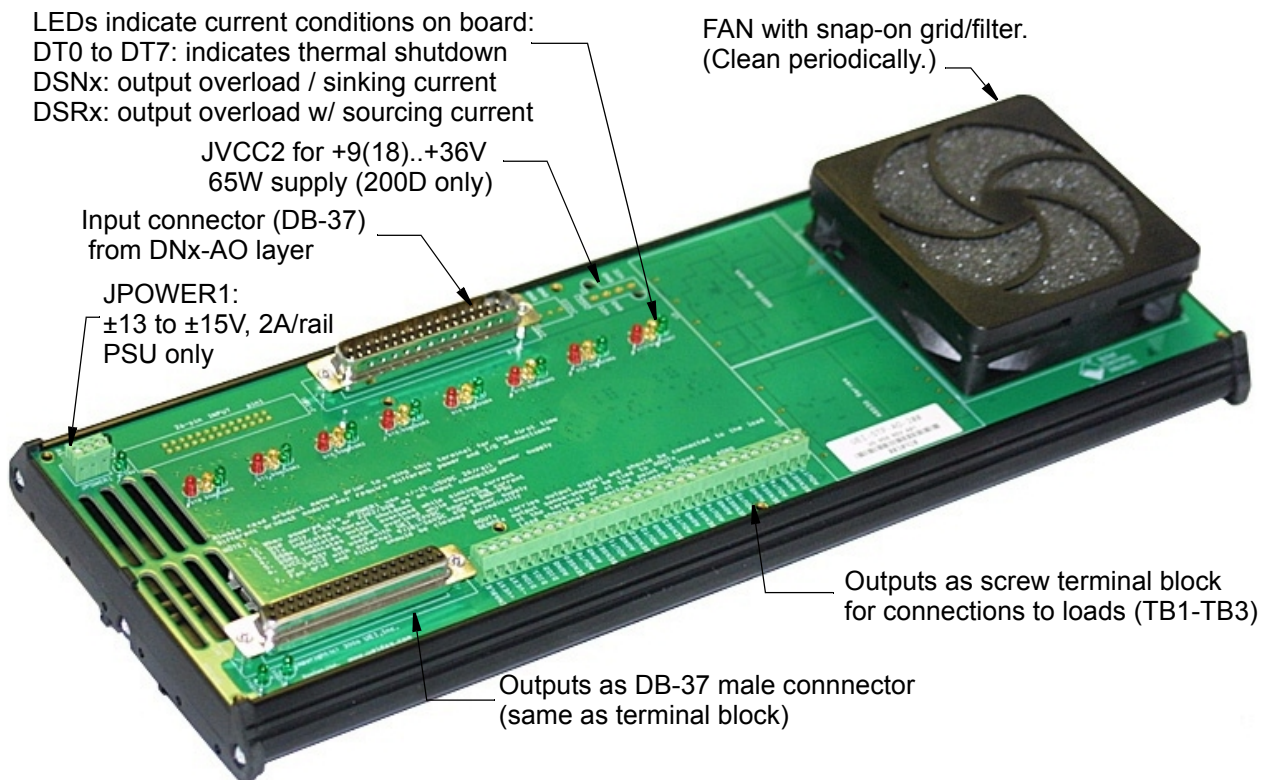


Figure 1-1. The UEI-STP-AO-200 Current Buffer Terminal Panel

Note that the top of the STP-AO-200 terminal panels, although not physically covered, has no active signals exposed except those on the various connectors and screw terminals.

1.5 Specification The technical specification for the STP-AO-200 is provided in the table below:

Table 1-1. UEI-STP-AO-200 Technical Specifications

Number of Channels	8
Gain	Unity gain (x 1)
Gain error	±0.2%, max
Offset error	±3 mV, max
Output slew rate	1 V/μS
Output current	250 mA, at ±10V, min
Output protection	500 mA, resettable fuse per channel
Output overload display	LEDs display current source/sink > 280 mA
Thermal overload	LED display of output buffer over temp
Input power required	±13 to ±15 V
Input power required with optional DC/DC	+9 to +36 VDC
Cooling	Fan based forced air cooling. Fan turns on when internal temperature exceeds 45°C.
Physical Dimensions	10.0" x 4.2" x 2.125"
Operating Temp. (tested)	0°C to +85°C
Storage Temp	-40°C to +85°C
Operating Humidity	5 - 90%, non-condensing

1.6 Wiring & Connectors

The pinout for the STP-AO-200 DB-37 connectors is provided in **Figure 1-2**. These include the J302/308 female input connector from the DNA-AO layer and JOUT1 male output connector that provides the outputs of the screw terminal panel as a DB-37 output.

**DB-37 (female)
 37-pin Connector:**

AOUT0 SNS	37	19	AGND
AGND	36	18	AOUT0
AOUT1	35	17	AOUT1 SENSE
AOUT2 SNS	34	16	AGND
AGND	33	15	AOUT2
AOUT3	32	14	AOUT3 SENSE
AOUT4 SNS	31	13	AGND
AGND	30	12	AOUT4
AOUT5	29	11	AOUT5 SENSE
AOUT6 SNS	28	10	AGND
AGND	27	9	AOUT6
AOUT7	26	8	AOUT7 SENSE
+VEXT	25	7	AGND
AGND	24	6	-VEXT
AGND	23	5	AGND
DIO2	22	4	DIO1
AGND	21	3	DIO0
-15V (20mA) OUT	20	2	+15V (20mA) OUT
		1	AGND

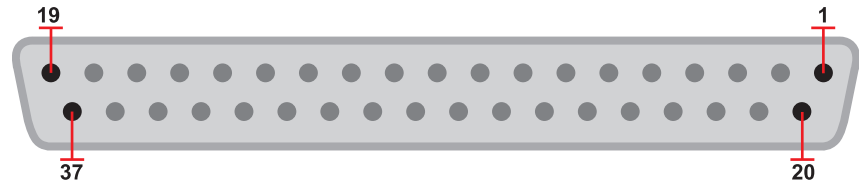


Figure 1-2. Pinout of UEI-STP-AO-200 input connector

The TB outputs are described as follows:

Line	Description
ENABLE	Enabled by default. When grounded disables current output.
VEXT	Carries +VEXT and -VEXT voltages.
DIO	Digital input lines 0 to 2 (for layers that support this feature).
AGND	The channel's ground reference line.
SENSE	Output sense-should be tied to channel's AOUT at the terminal or at the point of load. Load should be connected between AOUT and AGND.
AOUT	carries output signal and should be connected to the load.

Table 1-2. Terminal Block outputs

Appendix A

A. Accessories

The following cables and I/O Layers are available for the UEI-STP-AO-200.

DNA-CBL-37

This is a 37-conductor cable with 37-pin D-sub connectors on both ends; 3 ft long. Connects the UEI-STP-AO-200 to the DNx-AO-308 or similar layer.



Figure A-1. Photo of DNA-CBL-37 Flat Ribbon Cable

DNA-CBL-37S - Round, Shielded Cable

This is a 37-conductor cable with 37-pin D-sub connectors on both ends. Connects an STP-AO-200 screw terminal master panel to a similar slave panel. It is made with round, heavy-shielded cable; 2.5 ft (75 cm) long, weight of 9.49 ounces or 269 grams; up to 10ft (305cm) and 20ft (610cm).



Figure A-2. Photo of DNA-CBL-37 Round, Shielded Cable

Index

C

Cable(s) 9
Conventions 4

F

Features 6

L

Layout 6

O

Organization 3
Outputs 8

P

Pinout 8

S

Screw Terminal Panels 9
Setting Operating Parameters 6
Specification 7
Support ii

T

Terminal Panels 6