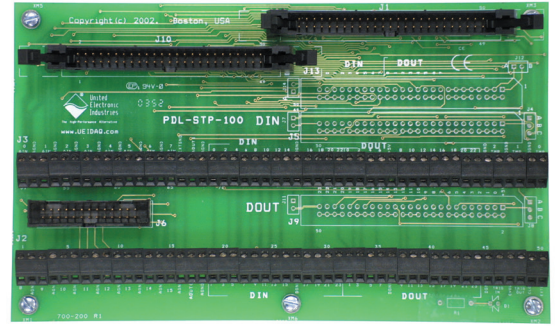


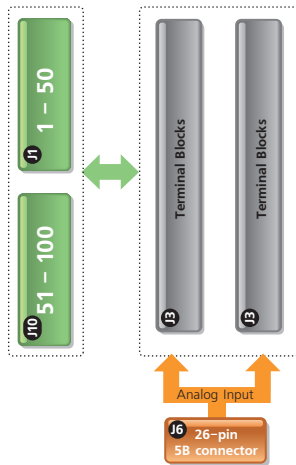
# PDL-STP

## 100-way terminal panel for PDL-MF cards

- Designed for PowerDAQ Lab multifunction cards (PDL-MF, PDL-MF-333)
- 16 analog input lines, 2 analog output lines, 24 digital input and 24 digital output lines, 3 counter/timer lines
- Can be connected with 5B signal-conditioning racks
- 107 x 179 x 38 mm



### Block Diagram:



### Pinout Diagrams:

**J1 — IDC-50 (male)  
50-pin header:**

AIN8	1	2	AGND1
AIN9	3	4	AGND2
AIN10	5	6	AGND3
AIN11	7	8	AGND4
AIN12	9	10	AGND5
AIN13	11	12	AGND6
AIN14	13	14	AGND7
AIN15	15	16	AGND8
AOUT1	17	18	AGND9
DIN1	19	20	DIN3
DIN5	21	22	DIN7
DIN9	23	24	DIN11
DIN13	25	26	DIN15
DGND1	27	28	DIN17
DIN19	29	30	DIN21
DIN23	31	32	DOUT1
DOUT3	33	34	DOUT5
DOUT7	35	36	DGND2
DOUT9	37	38	DOUT11
DOUT13	39	40	DOUT15
DOUT17	41	42	DOUT19
DOUT21	43	44	DOUT23
DGND3	45	46	EXT_TRIG_IN
CV_OUT	47	48	EXT_TRIG_OUT
CL_OUT	49	50	EXT_CLK

**J10 — IDC-50 (male)  
50-pin header:**

AINO	1	2	AGND10
AIN1	3	4	AGND11
AIN2	5	6	AGND12
AIN3	7	8	AGND13
AIN4	9	10	AGND14
AIN5	11	12	AGND15
AIN6	13	14	AGND16
AIN7	15	16	EXT_GND
AOUT1	17	18	AGND17
DINO	19	20	DIN2
DIN4	21	22	DIN6
DIN8	23	24	DIN10
DIN12	25	26	DIN14
DGND4	27	28	DIN16
DIN18	29	30	DIN20
DIN22	31	32	DOUT0
DOUT2	33	34	DOUT4
DOUT6	35	36	+5VPI2
DOUT8	37	38	DOUT10
DOUT12	39	40	DOUT14
DOUT16	41	42	DOUT18
DOUT20	43	44	DOUT22
DGND5	45	46	TMR2
DGND6	47	48	TMR1
DGND7	49	50	TMR0

**J6 — IDC-26 (male)  
26-pin header:**

AINO	1	2	AIN8
AGND10	3	4	AIN9
AIN1	5	6	AGND2
AIN2	7	8	AIN10
AGND12	9	10	AIN11
AIN3	11	12	AGND4
AIN4	13	14	AIN12
AGND14	15	16	AIN13
AIN5	17	18	AGND6
AIN6	19	20	AIN14
AGND16	21	22	AIN15
AIN7	23	24	AGND8
N/C	25	26	N/C

